


Profile

Name of the Faculty	S JYOTHIRMAYE	
Designation	ASSOCIATE PROFESSOR	
Department	ECE	
Area of Interest	COMMUNICATION, SIGNAL PROCESSING, IOT	
Subjects Taught	DSP,LIC,SS,ADC	
JNTUH Registration Id	58150402-103822	
College Staff Code	SC0147	
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Educational Qualifications:

S. No.	Degree	Specialization	University/College	Year
1	SSC	SSC	CBSE	1996
2	INTERMEDIATE	M.BiPC	CBSE	1998
3	BE	ECE	MIT MANIPAL, VTU	2002
4	M TECH	EMBEDDED SYSTEMS	JNTUH	2010

Research Publications:

S. No.	Publication details
1	S Jyothirmaye, Dr.S.Spandana,“Breast Masses classification using contour shape description based on Beam Angle Statistics”, 3rd International Conference on Inventive Research in Computing Applications(ICIRCA 2021),September,2021.
2	S Jyothirmaye , RVNR Suneel Krishna,“ Low power SAR-ADC in 0.81 μm Mixed Mode CMOS process for Biomedical Applications” ,IOSR Journal of VLSI and Signal Processing ,International Journal, Volume III, October, 2013.
3	S.Jyothirmaye, B.Ramu, “Border Guard Spy Robot”, Volume 81, Test and Managment Engineering,The Mattingley Publishing Co., Inc., December, 2019.
4	S.Jyothirmaye, Dr.V.Satya Srinivas, “Fast Satellite Selection Techniques and DOPs for Multi-GNSS Positioning”, URSI International Conference(IEEE), March, 2019.
5	S.Jyothirmaye, B.Ramu, “Performance Analysis of SBAS Aided GNSS Positioning”, URSI International conference (IEEE), March, 2019.
6	S.Jyothirmaye, Y.Sivarama Krishna, “Low-Complexity Architecture for PAPR Reduction in OFDM Systems with Near-Optimal Performance” , International Journal of Professional Engineering Studies, July, 2017.
7	S.Jyothirmaye, P. Kalavathi, “Radar Signal Processing-Clutter Removal”, International Conference on Signal Processing, Communications & System Design, December, 2015.
8	S.Jyothirmaye, Dr.B.Ananda Krishna, “Survey on Challenges and Improving the Quality of Service in Mobile Ad Hoc Networks” , Third National Conference on Latest Trends in Signal Processing, VLSI and Embedded Systems, June, 2014.

9	S Jyothirmaye, “Design of High Performance 64 Bit MAC Unit”, International Journal of Research in Electronics and Communication Technology, July-September, 2014.
10	S Jyothirmaye, “VLSI Architecture development for Advanced Encryption Standards”, IOSR Journal of VLSI and Signal Processing, October, 2013.
11	S Jyothirmaye, “Development and design of the ATM Banking security system through Image Processing and GSM Communication”, IOSR Journal of VLSI and Signal Processing, October, 2013.
12	S Jyothirmaye, “Distribution of time codes across the space wire computer networks used in onboard spacecraft”, National Conference on Emerging Trends in Signal Processing and Embedded Systems, February, 2012.
13	S Jyothirmaye , M.Muthamma, “Modified Set D-FF” National Conference on Emerging Trends in Signal Processing and Embedded Systems, February, 2012.
14	S Jyothirmaye, “Sharpness Enhancement and Noise Removal in images using Adaptive Bilateral Filters”, National Conference on Emerging Trends in Signal Processing and Embedded Systems, February, 2011.

Books/Book Chapters Published:

S. No.	Publication details
	NIL

Experience:

Teaching	16
Industry	NIL
Research	NIL
Total Experience	16

